

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: David CASEY
Title: BIPOLAR TRANSISTOR WITH A
LOW SATURATION VOLTAGE
Appl. No.: 10/566,813
International Filing Date: 07/12/2004
371(c) Date: 01/30/2006
Examiner: Jae Lee
Art Unit: 2823
Confirmation Number: 8053

REPLY UNDER 37 CFR 1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the Non-Final Office Action dated December 28, 2007, concerning the above-referenced patent application.

Remarks/Arguments begin on page 2 of this document.